

02-16-01

Case Docket No. NL 000139

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Jc960 U.S. PTO

THE COMMISSIONER FOR PATENTS, Washington, D.C. 20231

Enclosed for filing is the patent application of Inventor:
 YOURI PONOMAREV

FOR: SEMICONDUCTOR DEVICE WITH AN INTEGRATED CMOS CIRCUIT WITH MOS TRANSISTORS HAVING SILICON-GERMANIUM ($Si_{1-x}Ge_x$) GATE ELECTRODES, AND METHOD OF MANUFACTURING SAME

ENCLOSED ARE:

- ☒ Appointment of Associates;
- ☒ Information Disclosure Statement, Form PTO-1449 and copies of documents listed therein;
- ☒ Preliminary Amendment;
- ☒ Specification (12 Pages of Specification, Claims, & Abstract);
- ☒ Declaration and Power of Attorney:
 (1 Page of a ☒ fully executed ☐ unsigned Declaration);
- ☒ Drawing (4 sheets of ☐ informal ☒ formal sheet);
- ☒ Certified copies of European Application Nos. 00200540.3 and 00201028.8;
- ☒ Authorization Pursuant to 37 CFR §1.136(a)(3)
- ☐ Other: ;
- ☒ Assignment to U.S. PHILIPS CORPORATION.

FEE COMPUTATION

CLAIMS AS FILED				
FOR	NUMBER FILED	NUMBER EXTRA	RATE	BASIC FEE - \$710.00
Total Claims	8 - 20 =		X \$18 =	
Independent Claims	1 - 3 =		X \$80 =	
Multiple Dependent Claims, if any			\$270 =	
TOTAL FILING FEE				= \$ 710.00

Please charge Deposit Account No. 14-1270 in the amount of the total filing fee indicated above, plus any deficiencies. The Commissioner is also hereby authorized to charge any other fees which may be required, except the issue fee, or credit any overpayment to Account No. 14-1270.

☐ Amend the specification by inserting before the first line as a centered heading --Cross Reference to Related Applications--; and insert below that as a new paragraph --This is a continuation-in-part of application Serial No. , filed , which is herein incorporated by reference--.

CERTIFICATE OF EXPRESS MAILING


Express Mail Mailing Label No. EL 458 218 991 US

Date of Deposit: February 15, 2001

I hereby certify that this paper and/or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

 Patti DeMichele
 Typed Name

Signature


 Steven R. Biren, Reg. No. 26,531
 Attorney
 (914) 333-9630
 U.S. Philips Corporation
 580 White Plains Road
 Tarrytown, New York 10591

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

YOURI PONOMAREV,

NL 000139

Serial No.

U.S. PTO
09/784424
02/15/01

Filed: CONCURRENTLY

SEMICONDUCTOR DEVICE WITH AN INTEGRATED CMOS CIRCUIT WITH MOS TRANSISTORS HAVING SILICON-GERMANIUM ($Si_{1-x}Ge_x$) GATE ELECTRODES, AND METHOD OF MANUFACTURING SAME

Commissioner for Patents
Washington, D.C. 20231

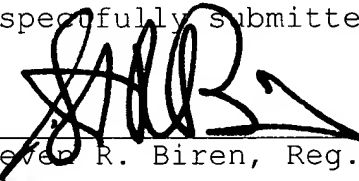
AUTHORIZATION PURSUANT TO 37 CFR §1.136(a)(3)
AND TO CHARGE DEPOSIT ACCOUNT

Sir:

The Commissioner is hereby requested and authorized to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time.

Please charge any additional fees which may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

Respectfully submitted,

By 
Steven R. Biren, Reg. 26,531
Attorney
(914) 333-9630